

Profile

Name: **Dr. N.M.SPENCER PRATHAP SINGH**

Designation: **PROFESSOR**

Qualification: **M.Tech;Ph.D**



He obtained his **B.E (Electrical and Electronics Engineering)** from **Manonmanium Sundaranar University** in **1998**. He completed his **M.Tech (Control Systems)** from **Kerala University** in **2005**. He obtained his **Ph.D** in the field of **power Electronics and Drives** from **Anna University**, Tamil Nadu, India during **June 2017**.

He is a **life member** of **ISTE**. His area of interest includes **Electromagnetism, Control Systems and Power Electronics and Drives**. He is a member of **IEEE**. He organized various conferences, seminars and workshops.

He joined this Institute on **28-11-1998** and having more than **24 years of teaching experience**. He presented papers in **04 conferences** and published papers in **06 Journals**

International Journals

1. **Spencer Prathap Singh & Kesavan Nair** 2013, 'Intelligent Controller for Reduction of Total Harmonics in Single Phase Inverters', American Journal of Applied Sciences, vol. 10, no. 11, pp. 1378-1385. **(Annexure II, IF-0.79)**.
2. **Spencer Prathap Singh, N M & Kesavan Nair, N** 2014, 'Design of Mitigating Voltage and Current Harmonics in Sine Wave UPS Inverter for Line-Load Variations', International Journal of Applied Engineering Research, vol. 9, no. 24, pp. 23565-23577, (Annexure II IF-0.14).
3. **Spencer Prathap Singh, N M & Kesavan Nair, N** 2015, 'Voltage and Current Harmonics Minimization in Single Phase Sine Wave Voltage Source Inverter Using Neuro-Fuzzy Controller', International Journal

of Advanced Research in Electrical, Electronics and Instrumentation Engineering, vol. 4, no. 12, pp. 9840-9849.

4. **Spencer Prathap Singh, NM**, Kesavan Nair & Ajith Bosco Raj 2016, 'Distorted Waveform Balancing using an Artificial Bee Colony (ABC) Based Optimal Control for Mitigating Total Harmonics in Single Phase Inverter', Circuits and Systems, vol. 7, no. 9, pp. 2154-2167, **(Annexure I, IF-0.88)**.
5. **Spencer Prathap Singh, NM** & Kesavan Nair, N 2017, 'Artificial Bee Colony Algorithm for Inverter Complex wave Reduction under Line-Load Variations', Transactions of the Institute of Measurement and Control, vol.40, no.5, pp.1593-1607. **(Annexure I, IF-0.820)**.
[<http://journals.sagepub.com/doi/10.1177/0142331216687019?ai=10x&ui=201pd&af=T>]
6. **Spencer Prathap Singh, NM** & Subin Hans, VN 2014, 'Minimization of Harmonics in Single Phase Sine Wave Inverter Fed Unity Power Factor Load', Proceedings of International Conference on Recent Trends in Engineering and Technology, vol. 1, pp. 407-410.

Achievements:

Peer reviewer for	1.International Journal of Electronics 2. Journal of Electrical Engineering and Technology.
Conference Chair	1.International conference at C.S.I Institute of Technology, Thovalai 2.International conference at PET Engineering College, Vallioor.